

2812
PATENT

#6/DKung

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Nayon Tomsio et al.

Assignee: Sun Microsystems, Inc.

Title: RESISTIVE VIAS FOR CONTROLLING IMPEDANCE AND
TERMINATING I/O SIGNALS AT THE PACKAGE LEVEL

Application No.: 10/028,814

Filed: December 20, 2000

Examiner: (not yet assigned)

Group Art Unit: 2812

Atty. Docket No.: 004-6459 (formerly
P6459/SUN-01-02)

Conf. No.: 5544

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JP2800 MAIL ROOMCOMMISSIONER FOR PATENTS
Washington, DC 20231**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(INCLUDING REVOCATION OF PRIOR POWERS)**

Dear Sir:

Assignee of record of the entire interest of the above-identified application hereby
revokes all powers of attorney previously given and appoints the following attorney(s) and/or
agent(s) to prosecute this application and to transact all business in the United States Patent and
Trademark Office connected therewith:

Mark Zagorin (36,067); Andrew C. Graham (36,531); David W. O'Brien (40,107);
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Pavel Pogodin (48,205), and Jeffrey L. Myers (44,252).

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22120

PATENT TRADEMARK OFFICE

**STATEMENT UNDER 37 CFR § 3.73(b)
ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION**

The assignee, Sun Microsystems, Inc., seeks to take action in the above-identified patent application. An assignment of the above-identified patent application from the inventors to the assignee is recorded in the Patent and Trademark Office at Reel 012702/Frame 0495.

The undersigned, whose title is given below, is authorized to act on behalf of the assignee.

11/1/02
Date

Marilyn E. Glaubenslee
Assistant General Counsel
Sun Microsystems, Inc.